

ACC CYFRONET AGH organises PRACE code porting workshop in Cracow, Poland

Academic Computer Centre CYFRONET AGH, in Cracow, Poland organises a PRACE code porting workshop. The workshop will be held on 14–16 October in Cracow, Poland, at the same time with the Cracow Grid Workshop 2009.

The workshop will focus on specific aspects of NEC and BlueGene HPC (High Performance Computing) solutions by renowned experts in the area. The workshop is addressed to researchers and students from Europe who are interested in the state-of-the-art HPC infrastructures and practical introduction into porting applications to them.

Registration is open until October 4th, 2009 on the below mentioned website. The workshop attendance is free of charge, however the attendees have to cover their travel and lodging in Cracow. The maximum number of participants in the event is limited to 50 people.

The workshop attendees will have a chance to have a hands-on experience with programming on HPC systems including BlueGene/P at FZJ (Juelich) and NEC at HRLS (Stuttgart) and will receive user accounts on these machines which will be valid during the workshop's hands-on sessions. The participants will have a chance to get experience with code porting on real machines during the workshop's sessions.

The first day of the workshop will provide introductory presentations into the subject and the next two days will hold the actual workshop. First half of each day will be dedicated to one machine and the second to other so that on the second day the attendees have a chance to discover differences between these systems.

Workshop speakers are Ph. D. **Lukas Arnold**, FZJ, Germany; Ph. D. **Kamil Iskra**, Argonne National Laboratory, US and **Harald Klimach**, University of Stuttgart.

Registration and agenda: <http://fivo.cyf-kr.edu.pl/prace>

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About PRACE: The Partnership for Advanced Computing in Europe (PRACE) prepares the creation of a persistent pan-European HPC service, consisting of several tier-0 centres providing European researchers with access to capability computers and forming the top level of the European HPC ecosystem. PRACE is a project funded in part by the EU's 7th Framework Programme (FP7/2007-2013) under grant agreement n° RI-211528.